

REMARKS

The Examiner's Office Action of December 10, 2003 has been received and its contents reviewed. By this Amendment, claim 1 has been amended, claims 2-20 have been cancelled and claims 21-52 have been added. No new matter has been added by these amendments. Support for the amendments and new claims can be found, for example, in the descriptions associated with FIGs. 1, 12 and 13. By the actions above and the remarks below, Applicants respectfully request reconsideration and allowance of all the pending claims.

Referring now to the detailed Office Action, in paragraph 1, FIG. 24 is objected to as requiring a designation of --Prior Art--. Attached hereto is a substitute drawing sheet that provides the label "Related Art" to FIG. 24 since it is specifically discussed in the "Description of Related Art" section of the specification. Accordingly, Applicants respectfully request reconsideration and withdrawal of the objection.

Starting on page 2 of the Office Action, claims 1, 2, 7, 9 and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 5,452,250 to Riggio Jr. et al. (hereinafter "Riggio Jr."); claims 17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Riggio Jr, in view of Applicants' admitted prior art; and claims 3-6, 8, 11, 12, 15 and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Riggio Jr. in view of U.S. Patent No. 5,798,744 to Tanaka et al. (hereinafter "Tanaka") and U.S. Patent No. 4,305,083 to Gutierrez. Applicants respectfully traverse this rejection in view of the amendments provided above and the comments to follow.

The Riggio Jr. patent is directed to a memory controller that generates control and address signals for accessing a non-volatile memory having a plurality of addressable cells (Abstract).

The Tanaka patent is directed to a liquid crystal display apparatus having a pair of substrates, at least one of which is transparent, and a liquid crystal layer formed by enclosing a liquid crystal composition between the pair of substrates. (Abstract).

The Gutierrez patent is directed to a single charge injector floating gate memory cell where an injector diode is defined by an ion implanted region of opposite conductivity from that of the semiconductor substrate and by a diffused region (Abstract).

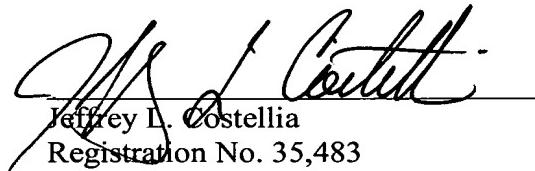
The presently claimed invention, as now set forth in amended claim 1, is directed to

an electronic device. The electronic device includes a pixel region including a first thin film transistor over a substrate. A source signal line side driver is operationally connected to the pixel region. The source signal line side driver comprises a second thin film transistor over the substrate. A gamma correction control circuit is operationally connected to the source signal line side driver. The gamma correction control circuit comprises a third thin film transistor over the substrate. The electronic device also includes a nonvolatile memory operationally connected to the gamma correction control circuit. The nonvolatile memory comprises a fourth thin film transistor over the substrate. Applicants submit that none of Riggio, Jr., Tanaka or Gutierrez discloses or suggests all features of the presently claimed invention.

In view of the amendments and arguments set forth above, Applicants respectfully request reconsideration and withdrawal of all the pending rejections.

While the present application is now believed to be in condition for allowance, should the Examiner find some issue to remain unresolved, or should any new issues arise which could be eliminated through discussions with Applicants' representative, then the Examiner is invited to contact the undersigned by telephone in order that the further prosecution of this application can thereby be expedited.

Respectfully submitted,



Jeffrey L. Costellia
Registration No. 35,483

NIXON PEABODY LLP
Suite 900, 401 9th Street, N.W.
Washington, D.C. 20004-2128
(202) 585-8000

JLC/BCO